

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10810035				
<b>Filing Date:</b>	26-Mar-2004				
<b>Title of Invention:</b>	Non-volatile transistor memory array incorporating read-only elements with single mask set				
<b>First Named Inventor/Applicant Name:</b>	Albert S. Weiner				
<b>Filer:</b>	Thomas Schneck/Merle Garcia				
<b>Attorney Docket Number:</b>	ATM-291				
Filed as Large Entity					
<b>Utility Filing Fees</b>					
<b>Description</b>		<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>					
<b>Pages:</b>					
<b>Claims:</b>					
<b>Miscellaneous-Filing:</b>					
<b>Petition:</b>					
<b>Patent-Appeals-and-Interference:</b>					
<b>Post-Allowance-and-Post-Issuance:</b>					
<b>Extension-of-Time:</b>					

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	790	790
<b>Total in USD (\$)</b>				<b>790</b>